



# Electric Shock Protector For the Tube Light

Parameters Subject to Change Without Notice

#### **DESCRIPTION**

JW<sup>®</sup>JW1818B aimed at reducing the risk of electric shock duringthe re-lamping.

Patented protection strategies ensure the reliability of the human body detection, which makes the assembling safer.

Supplied from the line directly makes JW1818B a quick start-up and lower BOM cost, and the low consumption technics lowers the detection current to the minimum.

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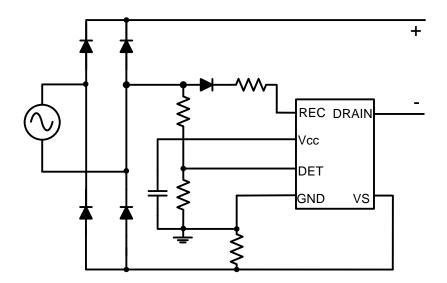
#### **FEATURES**

- Supplied from Line Directly
- Input Voltage Range20V~500V
- 600V power MOSFET integrated
- Programmable Detection Threshold
- Ultra- low Quiescent Current
- Short Delay for the Load
- SOP8 Package

## **APPLICATIONS**

LED lighting

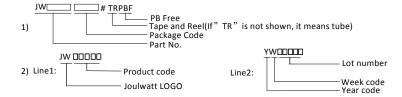
# **TYPICAL APPLICATION**



### **ORDER INFORMATION**

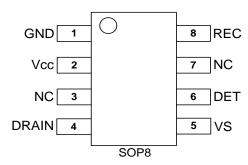
DEVICE <sup>1)</sup>	PACKAGE	TOP MARKING <sup>2)</sup>
JW1818BSOPB#TRPBF	CODS	JW1818B
	SOP8	YW□□□□□

#### Notes:



# **PIN CONFIGURATION**

#### **TOP VIEW**



# ABSOLUTE MAXIMUM RATING<sup>1)</sup>

RECPIN	650V
DRAIN PIN	600V
DET PIN	0.3V to 6V
VS PIN	6V to 0.3V
VCC PIN	0.3V to15V
Junction Temperature2) 3)	150°C
Lead Temperature	260°C
Storage Temperature	

#### RECOMMENDED OPERATING CONDITIONS

REC, DRAIN PIN	20V to 550V		
DET PIN	0.3V to5V		
VS PIN	5V to 0.3V		
VCC PIN	0.3V to14V		
Junction Temperature (T <sub>J</sub> )	150°C		
THERMAL PERFORMANCE <sup>4)</sup> SOP8	<b>θ</b> <sub>JA</sub> <b>θ</b> <sub>JC</sub> 9645°C/W		

#### Note:

- 1) Exceeding these ratings may damage the device.
- 2) The JW1818Bguarantees robust performance from -40°Cto 150°C junction temperature. The junction temperature range specification is assured by design, characterization and correlation with statistical process controls.
- 3) The JW1818B includes thermal protection that isintended to protect the device in overload conditions. Thermal protection is active when junctiontemperature exceeds the maximum operating junction temperature. Continuous operation over the specified absolutemaximum operating junction temperature may damage the device.
- 4) Measured on JESD51-7, 4-layer PCB.

# **ELECTRICAL CHARACTERISTICS**

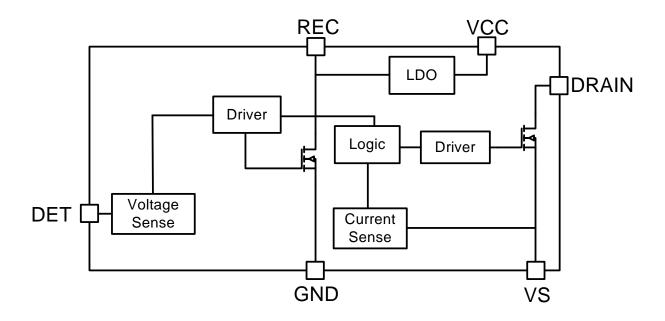
VREC = 35V, TA = 25°C, unless otherwise stated.						
Item	Symbol	Condition	Min.	Тур.	Max.	Units
Vcc Start -up Threshold	Vst		11	13	15	V
Vcc UVLO Threshold	Vcc <sub>UVLO</sub>		7.6	8.2	8.8	V
Quiescent Current	IQ	REC =30V		55	100	μΑ
DET Threshold	$V_{DET}$		0.94	1.0	1.06	V
VS Reference Voltage	Vs		-1.05	-0.99	-0.93	V
Detection time	T1		32	37	42	uS
Detection MOSFET Rdson <sup>5)</sup>	Rdson₁			33	38	Ω
Detection MOSFET Saturation  Current <sup>5)</sup>	I <sub>SAT</sub>		530			mA
Detection MOSFET Break-down Voltage	$V_{\text{BV1}}$	Id=150uA, DET=0V.	650			V
M MOOFFT	Rdson <sub>2</sub>			1.9	2.4	Ω
Main circuit power MOSFET	V <sub>BV2</sub>		600			V

<sup>5)</sup> Guaranteed by design

# **PIN DESCRIPTION**

Pin	Name	Description	
1	GND	Ground Pin.	
2	Vcc	Power supply pin. This pin supplies current to the internal start-up circuitry.  This pin must be locally bypassed with a capacitor.	
3,7	NC	No connection	
4	DRAIN	Drain of the internal power MOSFET.	
5	VS	Current sense pin. This pin set the current threshold to detect if there is leakage/electric shock or not.	
6	DET	Input voltage detection pin. This pin set the impedance detection threshold of input voltage by a divider connected between REC and GND.	
8	REC	High voltage power supply pin and the Drain of internal power MOSFET for impedance detection.	

# **BLOCK DIAGRAM**



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#### **FUNCTIONAL DESCRIPTION**

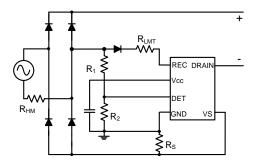
JW1818B is used to reduce the risk of electric shock during the re-lamping.

#### Start Up

JW1818Bis supplied by line voltage directly. When REC charges VCC up to 13V, the internal detection circuit starts to work, and it will maintain VCC voltage to 13V.Once VCC is lower than 8V, systemresets.

#### **Electric Shock Protection**

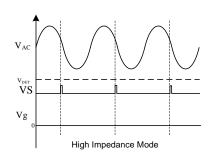
The impedance detection circuits samples the input current through VS pin.

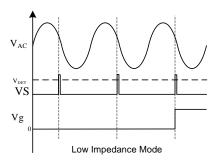


When DET pin is lower than 1.0 V and the internal MOSFET will be turned on for about 35uS as the figure shows below. The detection threshold can be calculated as:

$$V_{\text{DT}} = V_{\text{DET}} \times \frac{R1 + R2}{R2}$$

Where  $V_{\text{DET}}$  is the DET threshold, 1.0V in typical. If VS is lower than -1Vfor consecutive three detection cycles, JW1818B works in low impedance mode. The internal main circuit power MOSFET will be turned on and the impedance detection function disabled until Vcc restarts. If VS is higher than -1V, JW1818B works in the high impedance mode, which means electric shock happens. The internal power MOSFET will be turned off until the next detection cycle. The time sequences of the key signals are shown below.





Vg is the gate signal of the main circuit power MOSFET.

To limit the peak input current and protect the internal detection MOSFET,  $R_{LMT}$  is necessary. The recommended detection current should be lower than 0.53A at all conditions.

$$\frac{VBus_{PK}}{R_{LMT} + Rdson + RS} < 0.53A$$

Where,

 $VBus_{PK}$  is the peak Value at the maximum input voltage. For example,  $VBus_{PK}$  at 277V RMS line input

$$VBus_{PK} = 277V * \sqrt{2} = 391.7V$$

Rdson is the on resistance of the detection MOSFET,  $33\Omega$  in typical.

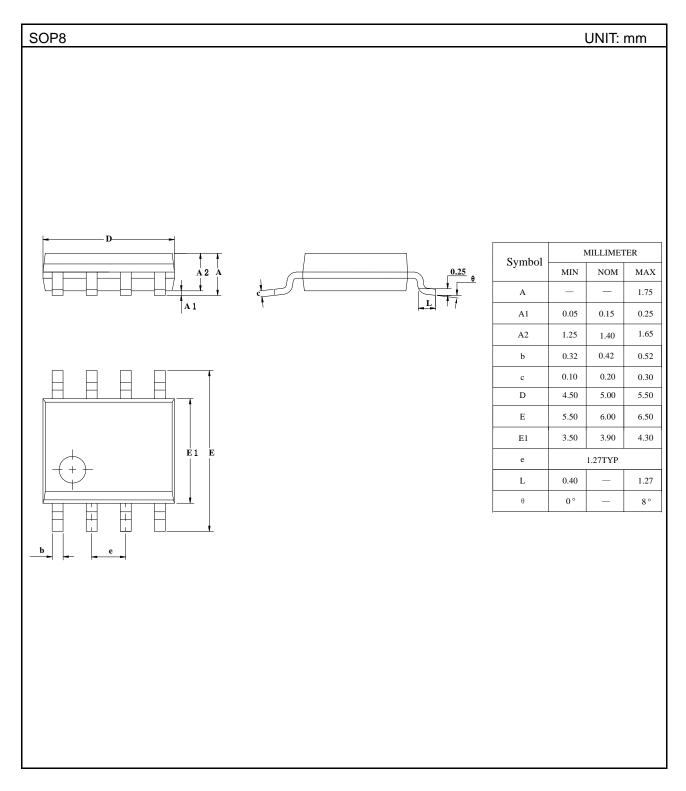
To ensure the reliable detection, make sure

$$\begin{cases} \frac{V_{\text{DT}}RS}{R_{\text{LMT}} + Rdson + RS} > 1.1V_{\text{DET}} \\ \frac{V_{\text{DT}}RS}{R_{\text{LMT}} + Rdson + RS + R_{\text{HM}}} < 0.9V_{\text{DET}} \end{cases}$$

# **PCB Design Guideline**

- JW1818B should be placed far away from the power devices for better thermal performance.
- 2. The VCC pin must be locally bypassed with a capacitor.
- 3. The area of LED current loop should be as small as possible.

# **PACKAGE OUTLINE**



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